



**PATENT**  
**29936/39848**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants:

**YOON et al.**

Serial No.: 10/734,804

Filed: December 12, 2003

For: DATA INPUT UNIT OF  
SYNCHRONOUS  
SEMICONDUCTOR MEMORY  
DEVICE, AND DATA INPUT  
METHOD USING THE SAME

Group Art Unit: 2818

Examiner: Gene Nghia Auduong

) I hereby certify that this paper and the  
) documents referred to as enclosed  
) therewith are being deposited with the  
) United States Postal Service as first  
) class mail, postage prepaid, on  
) **January 11, 2005**, in an envelope  
) addressed to Commissioner for  
) Patents, P.O. Box 1450, Alexandria,  
) Virginia 22313-1450.

) Michael R. Hull  
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**COMMENT ON STATEMENT OF REASONS FOR ALLOWANCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

In addition to the reasons for allowance set forth in the allowance papers that were mailed in connection with the present application, it is respectfully submitted that the claims are allowable for the additional reasons that the invention defined by the language of the claims is neither anticipated by, nor would have been obvious when taken as a whole in view of, the art of record.

Respectfully submitted,

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January 11, 2005

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